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ПвЭгПу-132 1x1200 ТУ У 31.3-00214534-060:2011



Power cables with copper conductor, with XLPE, longitudinal screen sealing and strengthened polyethylene outer sheath

For the cable of this mark correspond the foreign-made analogues: $\Pi B \Pi y \Gamma$ (RU)

Technical cable requirements correspond to IEC 60840

Cables are used for laying:

in soil (trenches)

• on difficult route sections, according to the unique specification

• in the air, including cable structures, if provided the additional fire protection

It is possible to manufacture cables with extruded semiconductor layer along outer sheath. Order entry example:

ПвЭгПу-П-132 1х1200/95 ТУ У 31.3-00214534-060:2011

An extruded semiconductor layer along outer sheath ensures the correct testing of cable line with sections of underground laying in polymer pipes.

It is possible to manufacture cables with an integrated fiber-optic module.

Order entry example:

ПвЭгПу-132 1х1200/95 (ОМ) ТУ У 31.3-00214534-060:2011

In conjunction with the DTS system, the integrated fiber-optic module can act as a distributed cable line temperature sensor.

It is possible to manufacture cable with sealed conductor. Order entry example: $\Pi B \Im \Gamma \Pi y$ -132 1x1200/95 (Γ) TY Y 31.3-00214534-060:2011





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TECHNICAL SPECIFICATIONS

Aaximum voltage Conductor rated area Ainimum screen cross-section	kV mm ² mm ²	145 1200
		1200
linimum screen cross-section	mm^2	1200
	111111	35
Partial discharge factor for rated voltage, not more than	рС	6
Permissible short circuit current across the screen of	kA	14.2
ninimum cross-section		
Aaximum permissible short-circuit current in core	kA	172
Permissible continious current rating by aerial laying *		
in trefoil formation with double-side screen earthing	А	1476
in trefoil formation with single-side screen earthing or	А	1746
cross screen earthing		
plane with double-side screen earthing	А	1358
plane with single-side screen earthing or cross screen	А	2086
earthing		
Permissible continious current rating by burial *		
in trefoil formation with double-side screen earthing	А	1012
in trefoil formation with single-side screen earthing or	А	1258
ross screen earthing		
plane with double-side screen earthing	А	824
plane with single-side screen earthing or cross screen	А	1337
parthing		
Aaximum permissible conductor temperature		
Continious	°C	+90
in emergency operation	°C	+130
at short circuit	°C	+250
Dperating temperature range	°C	-60 +50
Ainimum bending radius by laying	mm	1680
Rated outer diameter of the cable (for reference) **	mm	105
Cable weight (approximate)	kg/km	19120
Rated factory cable length and gross weight of the delivery on the drums ***	m, t	# 30УД-130: **** 373 • 10.0

Notes:

When ordering it is neccesary to agree the factory length of the product with the manufacturer

* Long permissible current loads are calculated for the following conditions: conductor temperature 90 °C, air temperature 30 °C, soil temperature 20 °C, load factor 1.0, thermal resistivity of soil 1.0 °K \cdot m/W, laying depth in the ground 1.5 m, while laying in flat formation the distance between cables in clear is equal to the cable diameter, while laying in trefoil formation cables are laid side by side ** The external diameter may differ from the rated up to ± 10 %

*** Отклонение фактической массы брутто от указанного значения может составлять \pm 7 %

**** Option delivery on not full drum

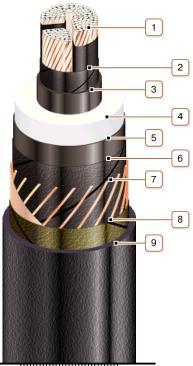




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CONSTRUCTION

- 1. Copper segmentary multiwire compact conductor Notes:
- It is possible to manufacture cable with sealed conductor.
 Conductor segment twisting is not illustrated
- 2. Lapping layer of semiconductive swellable tape
- 3. Inner extruded semiconducting layer
- 4. XLPE insulation
- 5. Outer extruded semiconducting layer
- 6. Lapping layer of semiconductive swellable tape

7. Copper screen

Note: It is possible to manufacture a cable with a fiber optic module built into the screen, including as a DTS system sensor

8. Lapping layer of nonwoven cloth tape

9. Strengthened polyethylene outer sheath

Note: It is possible to manufacture cable with extruded semiconductor layer along outer sheath

0 25 50 75